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138 in. The present invention relates to a
novel method of manufacturing a
semiconductor device and a
semiconductor device manufactured by
the method. As methods of forming
polycrystalline silicon of a semiconductor
layer, which is to be used in
semiconductor devices, such as a thin film
transistor (hereinafter referred to as a
TFT), as well as
polycrystalline silicon where a MOS
structure is formed and polycrystalline
silicon where a bipolar structure is
formed, as well as a method of
manufacturing a TFT, a method of

manufacturing a semiconductor device, a method of manufacturing a storage capacitor, and a method of manufacturing a display device, as disclosed in Japanese Patent Application Laid-Open Nos. 10-168688, 2001-148732, 2001-307577 and 2003-88991 are known. As for the method of manufacturing a semiconductor device, the method of forming a semiconductor layer using a chemical vapor deposition (CVD) method is known. As for the method of manufacturing a TFT and a method of manufacturing a semiconductor device, the polycrystalline silicon layer is formed by a CVD method and then an element separation process is carried out in the state where the polycrystalline silicon layer is formed. In such a process, the region where a single semiconductor layer is formed is a region where Si is deposited on the surface of a chamber. However, in a CVD method as described above, if the heating value of the chamber is high, the temperature of the chamber is increased. When the temperature of the chamber is increased, the polycrystalline silicon layer

itself is first deposited on a wafer and then is melted. As a result, silicon is deposited in the opening portion of the chamber and extends into the wall surface of the chamber to cause attachment of the wafer to the wafer chuck. Then, when the wafer is

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